Question 1 T/F A Shared Address Space programming model can be used on large scale multiprocessors.

Question 2 Which of the following statement is NOT true on the Centralized Shared-Memory Architectures?

The use of large multilevel caches can substantially reduce memory bandwidth demands of a processor.

Bandwidth of the centralized memory system grows as the number of processors in machines increases

It is possible for several (micro)processors to share the same memory through a shared bus.

Question 3 T/F Clusters have separate memories and thus need many copies of the operating system.

Question 4 Symmetric multiprocessors architectures, are sometimes known as

Variable memory access

Static memory access

Uniform memory access

Question 5 Which of the following systems is the least scalable with respect to its number of processors?

Cache-coherent NUMA systems

None

Symmetric multiprocessors

Noncache-coherent NUMA systems